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Amendments to the Claims

Please cancel claim 42 and amend the remaining claims as follows:

1-40. (Canceled)

- 41. (Currently Amended) A method of making a patterned semiconductor film, comprising the steps of:
 - a) inkjet printing, gravure printing, printing by offset lithography, or flexographic printing a solution composition comprising passivated semiconductor nanoparticles, a first cyclic Group IVA compound of the formula (1):

$$(AH_x)_n, (1)$$

where n is from 3 to 8 and each A in the formula is independently Si or Ge, and/or a second cyclic Group IVA compound of the formula (2):

$$(AH_x)_m(AH_yR_{z-y})_p(ZR')_q, (2)$$

where (m + p + q) is from 3 to 12, each of the m instances of x is independently 0, 1 or 2, each of the p instances of y is independently 0, 1 or 2, each of the p instances of z is independently 0, 1 or 2, each of the p instances of (y + z) is independently 1 or 2, each of the q instances of w is independently 0 or 1, at least one of p and q is at least 1, each A in the formula (2) is independently Si or Ge, Z is selected from the group consisting of B, P and As, R' is R or H, and each R in the formula (2) is independently alkyl, aryl, aralkyl, a halogen, BH₈R"_{2-s}, PH₅R"_{2-s}, AsH₅R"_{2-s}, or AH₅R"_{3-t}, where s is 0 to 2, t is 0 to 3, and R" is alkyl, aryl, aralkyl, a halogen, or AH₃, and a solvent in a pattern on a substrate; and

b) curing said printed composition pattern to form said patterned semiconductor film, wherein curing said printed composition pattern comprises irradiating said printed composition pattern, and said patterned semiconductor film comprises an

array of lines having a width of from 100 nm to 100 μ m, a length of from 1 μ m to 5000 μ m, and a thickness of from 0.01 μ m to 1000 μ m.

- 42. (Cancelled)
- 43. (Currently Amended) The method of Claim 4241, wherein—said semiconductor nanoparticles comprise <u>further comprising soluble</u> passivated semiconductor nanoparticles.
- 44. (Currently Amended) The method of Claim 4243, wherein said soluble passivated semiconductor nanoparticles comprise soluble passivated silicon nanoparticles.
- 45. (Currently Amended) The method of Claim 41, wherein said composition solution comprises both of said first and second cyclic Group IVA compounds.
- 46. (Currently Amended) The method of Claim 41, wherein said curing step comprises sintering said composition printed pattern to form said patterned semiconductor film.
- 47. (Canceled)
- 48. (Canceled)
- 49. (Canceled)
- 50. (Canceled)
- 51. (Currently Amended) The method of Claim 41, wherein said printing step further comprises selectively irradiating portions of said printed composition solution, and

removing either irradiated or non-irradiated portions of said printed eomposition-solution to form said pattern.

- 52. (Canceled)
- Original) The method of Claim 51, wherein said selectively irradiating substep comprises (i) positioning at least one of said substrate and a mask such that said portions can be selectively irradiated and said non-irradiated portions cannot be irradiated, and (ii) irradiating said layer with ultraviolet light through said mask.
- 54. (Original) The method of Claim 53, wherein said printing step further comprises the substep of aligning said mask to an alignment mark on said substrate.
- 55. (Canceled)
- 56. (Currently Amended) The method of Claim 41, wherein said printing step comprises inkjet printing said composition solution in said solvent in said pattern onto said substrate.
- 57. (Currently Amended) The method of Claim 41, wherein said printing step comprises gravure printing, offset lithography, or flexographic printing said eomposition solution in said solvent in said pattern onto said substrate.
- 58. (Currently Amended) The method of Claim 41, further comprising drying said composition solution and said substrate.
- 59. (Currently Amended) The method of Claim 43, wherein said curing step further comprises heating said composition pattern to a temperature of at least about 200 °C. to sinter said soluble passivated semiconductor nanoparticles and said composition pattern.

- 60. (Previously presented) The method of Claim 41, wherein said curing step further comprises placing said substrate into a chamber, and evacuating said chamber.
- 61. (Original) The method of Claim 60, wherein said curing step further comprises passing an inert and/or reducing gas into said chamber.
- 62. (Previously presented) The method of Claim 41, wherein said lines have a width of from
 0.5 to 50 μm.
- 63. (Original) The method of Claim 62, wherein said lines have an inter-line spacing of from
 100 nm to 100 μm.
- 64. (Previously presented) The method of Claim 62, wherein said lines have a length of from
 2 μm to 2000 μm.
- 65. (Currently Amended) The method of Claim 62, wherein said lines have a thickness of from 0.01 μm to 500 μm.

66-95. (Canceled)

- 96. (Currently Amended) The method of Claim 43, wherein said soluble passivated semiconductor nanoparticles comprise silicon nanoparticles and a passivation layer thereon.
- 97. (Currently Amended) The method of Claim 96, comprising wherein said passivation layer comprises at least one member selected from the group consisting of an alcohol, an alcoholate, a thiol, and a thiolate, an AR'₃ group, an alkyl group, an aryl group, and an aralkyl group.

- 98. (Currently Amended) The method of Claim 96, wherein said passivation layer comprises hydrogen and/or halogen atoms.
- 99. (Previously presented) The method of Claim 96, wherein said passivation layer further comprises a surfactant.
- 100. (Currently Amended) The method of Claim 43, wherein said soluble passivated semiconductor nanoparticles have an average particle diameter of less than 5 nm.
- 101. (Currently Amended) The method of Claim 43, wherein said soluble passivated semiconductor nanoparticles have a particle size distribution of from 0.2 nm to less than 10 nm.
- 102. (Currently Amended) The method of Claim 41, wherein the solution comprises comprising the first cyclic Group IVA compound of the formula (1).
- 103. (Previously presented) The method of Claim 102, wherein each x in the formula (1) is 2.
- 104. (Previously presented) The method of Claim 102, wherein each A in the formula (1) is Si.
- 105. (Previously presented) The method of Claim 102, wherein n is 5.
- 106. (Previously presented) The method of Claim 103, wherein each A in the formula (1) is Si.
- 107. (Previously presented) The method of Claim 103, wherein n is 5.

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- 108. (Previously presented) The method of Claim 106, wherein n is 5.
- 109. (Currently Amended) The method of Claim 41, wherein the composition solution consists essentially of said passivated semiconductor nanoparticles, said first cyclic Group IVA compound and said solvent.
- 110. (Currently Amended) The method of Claim 43, wherein the composition solution consists essentially of said soluble passivated semiconductor nanoparticles, said first and/or second cyclic Group IVA compounds, and said solvent.
- 111. (Currently Amended) The method of Claim 41, wherein the composition solution comprises said first and second cyclic Group IVA compounds, wherein p is 0 or 1, q is at least 1, (z y) is 0, and Z is B or P.
- 112. (Currently Amended) The method of Claim 111, wherein R' in the formula (2) is alkyl, aryl, or aralkyl.
- 113. (Previously presented) The method of Claim 41, further comprising a compound of the formula (ZH_uR_{3-u})_k, where Z is selected from the group consisting of B, P and As, u is an integer of from 0 to 3, k is 1 or 2, and R is the same as for the second cyclic Group IVA compound.
- 114. (Previously presented) The method of Claim 113, wherein R in the formula (ZH_uR_{3-u})_k is H or AH₃, where A is the same as for the second cyclic Group IVA compound.
- 115. (Previously presented) The method of Claim 113, wherein u is 0 or 3.

116. (Currently Amended) The method of Claim 41, wherein said first cyclic Group IVA compound is present in said composition solution in a percentage by weight of from 0.1% to 50%.

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- 117. (Currently Amended) The method of Claim 43, wherein said soluble passivated semiconductor nanoparticles, and said at least one of said first cyclic Group IVA compound and said second cyclic Group IVA compound are present in said ink in a percentage by weight of from 0.1% to 50%.
- 118. (Previously presented) The method of Claim 41, wherein said solvent is aprotic.
- 119. (Previously presented) The method of Claim 41, wherein said solvent is apolar.
- 120. (Previously presented) The method of Claim 118, wherein said solvent is apolar.
- 121. (Previously presented) The method of Claim 118, wherein said solvent has a boiling point of less than 250 °C. at atmospheric pressure.
- 122. (Previously presented) The method of Claim 121, wherein said solvent has a boiling point of less than 150 °C. at atmospheric pressure.
- (Currently Amended) The method of Claim 118, wherein said solvent is selected from the group consisting of alkanes, alkenes, halogenated alkanes, halogenated alkanes, halogenated alkanes, arenes, substituted arenes, ethers, cyclic ethers, aliphatic esters, aliphatic amides and aliphatic sulfoxides cycloalkanes.
- 124. (Currently Amended) The method of Claim 41, wherein said composition solution further comprises one or more additives selected from the group consisting of a tension reducing agent, a surfactant, a thickening agent, and a binder.

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125. (Previously presented) The method of Claim 59, wherein said sintering temperature is at least about 300 °C.

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- 126. (Currently Amended) The method of Claim 41, wherein said curing further comprises heating said cyclic Group IVA compound(s) to a temperature of at least about 100 °C. to dry the printed composition solution, prior to irradiating said printed composition pattern.
- 127. (Currently Amended) The method of Claim 126, wherein said curing step further comprises sintering said dried, irradiated composition pattern to form said patterned semiconductor film.
- 128. (Currently Amended) The method of Claim 41, comprising gravure printing said composition solution in said solvent in said pattern onto said substrate.
- 129. (Currently Amended) The method of Claim 41, comprising printing said composition solution in said solvent in said pattern onto said substrate by offset lithography.
- 130. (Currently Amended) The method of Claim 41, comprising flexographic printing said composition solution in said solvent in said pattern onto said substrate.
- 131. (Previously presented) The method of Claim 41, wherein curing is conducted under conditions sufficient to form a doped or undoped polysilane, polygermane or germanium-substituted polysilane having a molecular weight sufficiently high and/or a chemical composition sufficiently insoluble to resist subsequent treatment with processing solvents.
- 132. (Previously presented) The method of Claim 102, wherein at least one of the n instances of A is Ge.

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- 133. (Currently Amended) The method of Claim 98, wherein said composition solution further comprises a surfactant.
- (Previously presented) The method of Claim 133, wherein the surfactant comprises a tri-C₁-C₂₀ alkyl-substituted amine, a tri-C₁-C₂₀ alkyl-substituted amine oxide, a tetra-C₁-C₂₀ alkyl-substituted quaternary ammonium salt, a conventional betaine, a conventional sulfobetaine, a polyglycol of the formula H-(-OCH₂CH₂-)_a-OH (where 2 ≤ a ≤ 4), a polyether of the formula R³-(-OCH₂CH₂-)_a-OR⁴ (where R³ and R⁴ are independently a C₁-C₄ alkyl group), a C₄-C₂₀ branched or unbranched, saturated or unsaturated aliphatic carboxylic acid ester of a C₁-C₄ alcohol, a C₄-C₂₀ aliphatic carboxylic acid thioester of a C₁-C₄ thiol, a tri-C₁-C₂₀ alkyl- or triaryl-substituted phosphate, a di-C₁-C₂₀ alkyl- or diaryl-substituted phosphate salt, an aryl or C₄-C₂₀ branched or unbranched, saturated or unsaturated aliphatic sulfonic acid, an aryl or C₄-C₂₀ branched or unbranched, saturated or unsaturated aliphatic sulfonate, a di-C₁-C₂₀ alkyl sulfate, a C₁-C₂₀ alkyl sulfate salt, a ketone of the formula R⁵(C=O)R⁶ (where R⁵ and R⁶ are independently a C₁-C₂₀ alkyl or C₆-C₁₀ aryl group), and/or a conventional silicone.
- 135. (Previously presented) The method of Claim 44, wherein the silicon nanoparticles have an average diameter of less than 5 nm.
- 136. (Previously presented) The method of Claim 135, wherein the silicon nanoparticles have an average diameter of less than 3.5 nm.
- 137. (Previously Presented) The method of Claim 44, wherein the silicon nanoparticles have a size distribution range such that at least 95% of the nanoparticles have an average particle diameter of from 0.1 nm to 10 nm.

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- 138. (Previously presented) The method of Claim 137, wherein the silicon nanoparticles have a size distribution range such that at least 98% of the nanoparticles have an average particle diameter from 0.5 nm to less than 5 nm.
- 139. (Currently Amended) The method of Claim 116, wherein the first cyclic Group IVA compound is present in the composition solution in a percentage by weight of from 0.5 to 30 wt.%.
- 140. (Currently Amended) The method of Claim 139, wherein the first cyclic Group IVA compound is present in the composition solution in a percentage by weight of from 1.0 to 20 wt.%.
- 141. (Currently Amended) The method of Claim II7, wherein the soluble passivated semiconductor nanoparticles and first and/or second cyclic Group IVA compound(s) are present in the composition solution in a percentage by weight of from 0.5 to 30 wt.%.
- 142. (Currently Amended) The method of Claim 117, wherein the soluble passivated semiconductor nanoparticles and the first and/or second cyclic Group IVA compounds are present in a weight ratio of from 0.1% to 90%.
- 143. (Currently Amended) The method of Claim 117, wherein the soluble passivated semiconductor nanoparticles and the first and/or second cyclic Group IVA compounds are present in a weight ratio of from 10% to 50%.
- 144. (Previously presented) The method of Claim 41, wherein the solvent has a gas-phase dipole moment of about 2 debyes or less.
- 145. (Previously presented) The method of Claim 144, wherein the solvent has a boiling point of about or less than 200 °C. at atmospheric pressure.

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- 146. (Previously presented) The method of Claim 41, wherein the solvent has a gas-phase dipole moment of about 0.5 debye or less.
- 147. (Previously presented) The method of Claim 146, wherein the solvent has a boiling point of about or less than 150 °C. at atmospheric pressure.
- (Currently Amended) The method of Claim 133, wherein the surfactant is present in the composition solution in an amount of from 0.05 wt.% to 0.5 wt.% of the composition.
- (Currently Amended) The method of Claim 124, wherein the one or more additives are present in the composition solution in an amount of from 0.1 wt.% to 5 wt.%.
- 150. (Previously presented) The method of Claim 41, wherein the substrate comprises a semiconductor wafer or a transparent or translucent display window with a two-dimensional array of fields thereon.
- 151. (Currently Amended) The method of Claim 150, comprising inkjet printing, gravure printing, printing by offset lithography, or flexographic printing the eemposition solution in the pattern in each of the fields.
- 152. (Previously presented) The method of Claim 41, wherein the substrate comprises a glass or plastic window.
- 153. (Currently Amended) The method of Claim 41, further comprising irradiating portions of the printed eomposition solution with light having a wavelength and/or intensity sufficient to oligomerize or polymerize the irradiated portions of the eomposition solution.

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- 154. (Currently Amended) The method of Claim 41, wherein the portions of the printed composition with light solution are irradiated with light sufficiently to convert irradiated cyclic Group IVA compounds to an insoluble polymer.
- 155. (Currently Amended) The method of Claim 41, further comprising removing solvent from the printed eemposition solution prior to curing.
- 156. (Previously presented) The method of Claim 59, wherein said sintering temperature is at least 400 °C.
- 157. (Previously presented) The method of Claim 41, further comprising cleaning the substrate with the patterned semiconductor film thereon.
- 158. (Previously presented) The method of Claim 157, wherein cleaning comprises rinsing the substrate with or immersing the substrate in a cleaning solvent, draining the cleaning solvent from the substrate, and drying the substrate and patterned semiconductor thin film.
- 159. (Previously presented) The method of Claim 157, wherein the cleaning solvent comprises a solvent in which the first cyclic Group IVA compound has a high solubility.
- 160. (Previously presented) The method of Claim 62, wherein said lines have a width of from $1 \mu m$ to $20 \mu m$.
- 161. (Previously presented) The method of Claim 63, wherein said inter-line spacing is from 200 nm to 50 μm.
- 162. (Previously presented) The method of Claim 161, wherein said inter-line spacing is from 500 nm to 10 μm.

- 163. (Previously presented) The method of Claim 64, wherein said lines have a length of from 5 μm to 1000 μm .
- 164. (Previously presented) The method of Claim 65, wherein said lines have a thickness of from 0.05 μm to 250 μm .